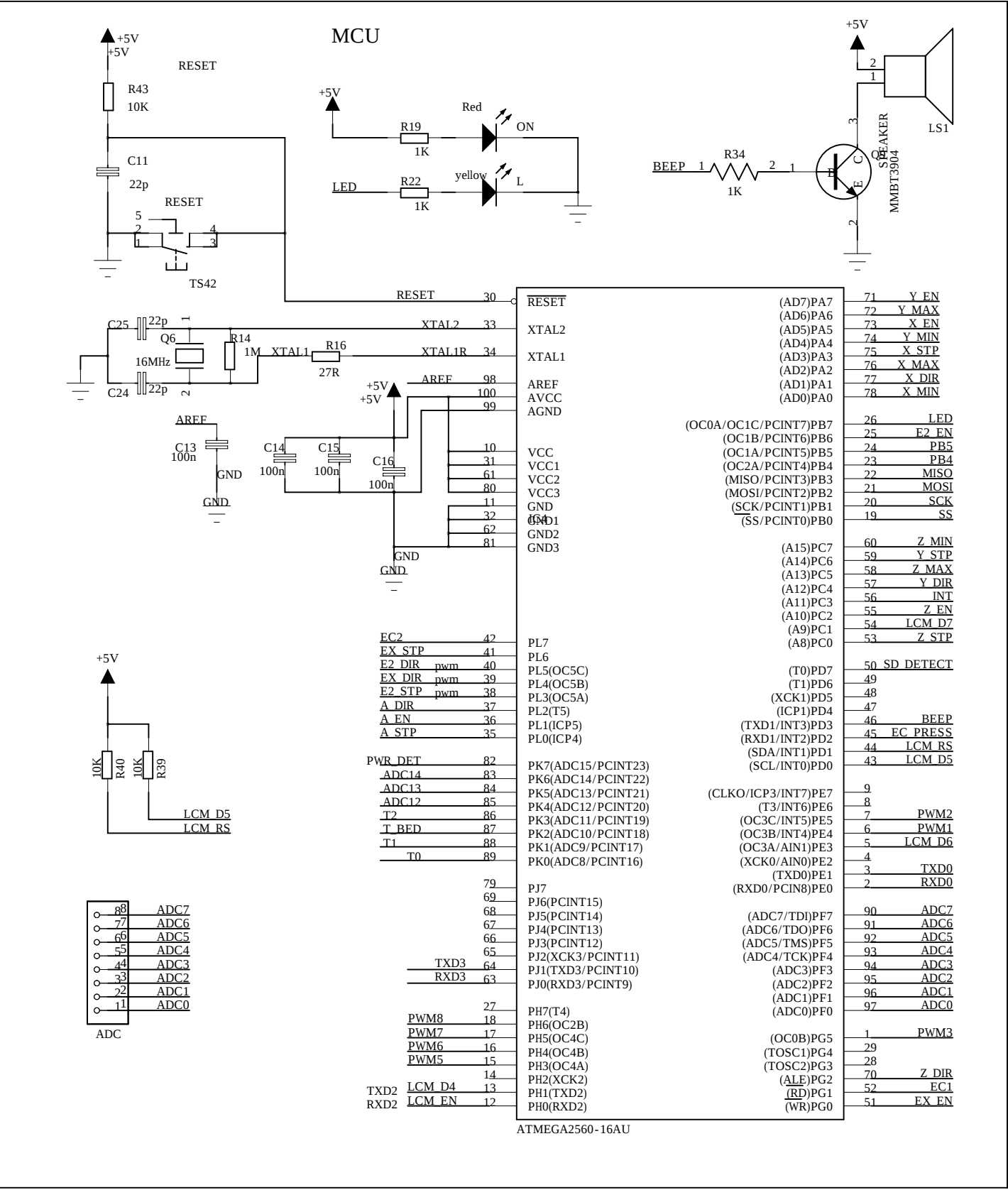
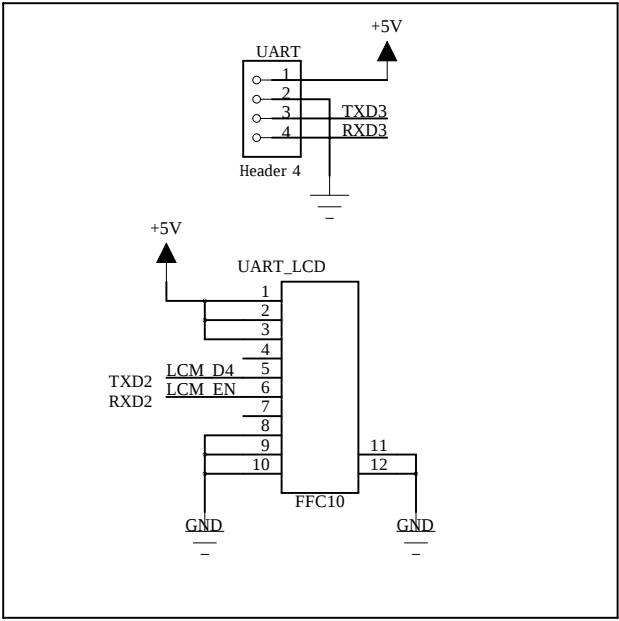
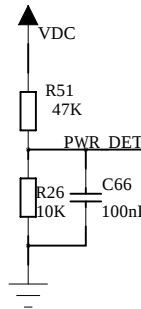




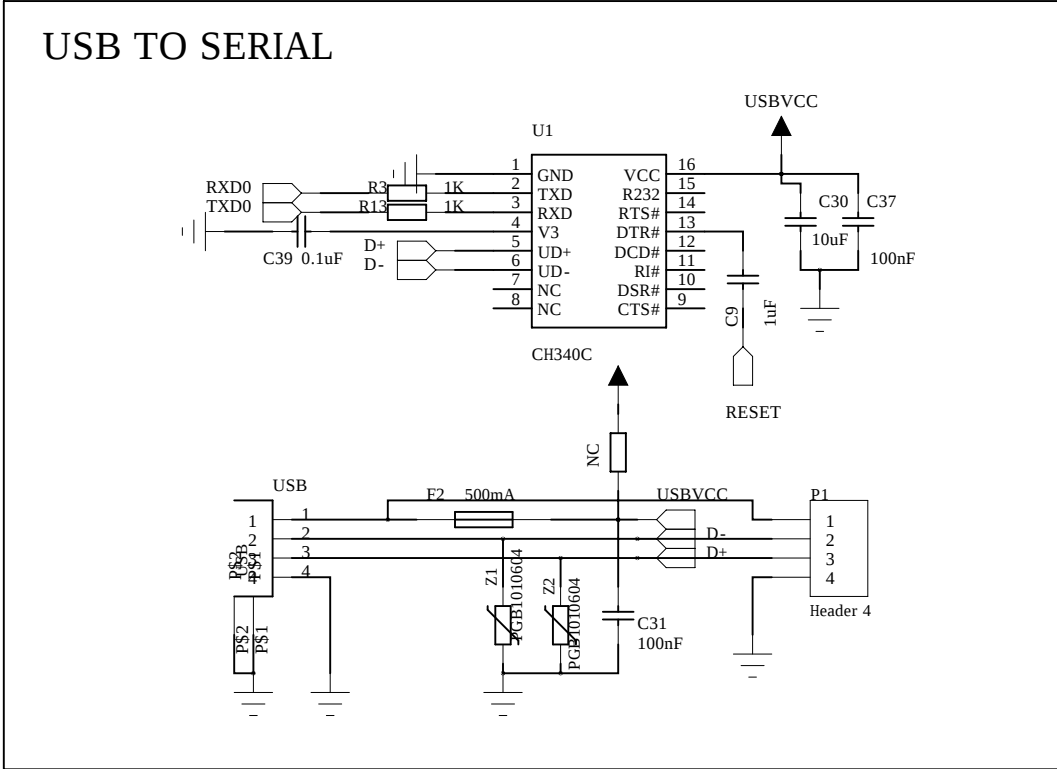
Pinout diagram of the FFC40 connector for the LCD and SD CARD module. The diagram shows a 40-pin connector with pins 1-40 on the left and pins 41-42 on the right. Pin 1 is +5V. Pins 2-9 are LCM D6, LCM D5, LCM D4, LCM D7, LCM EN, EC_PRESS, and BEEP. Pins 10-17 are unconnected. Pins 18-23 are EC1, EC2, RESET, and LCM RS. Pins 24-29 are unconnected. Pins 30-33 are SS, MOSI, SCK, and MISO. Pin 34 is unconnected. Pin 35 is SD_DETECT. Pins 36-40 are unconnected. Pins 41 and 42 are ground.



POWER DETC



The diagram shows a circuit for power detection. A vertical line represents a signal path. At the top, an arrow points upwards, labeled 'VDC'. Below this, a resistor labeled 'R51 47K' is connected in series. The signal path then splits into two parallel branches. The first branch contains a resistor labeled 'R26 10K' connected to ground. The second branch contains a capacitor labeled 'C66 100nF' connected to ground. The signal path then continues upwards, labeled 'PWR_DET'.

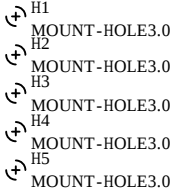


3D TOUCH

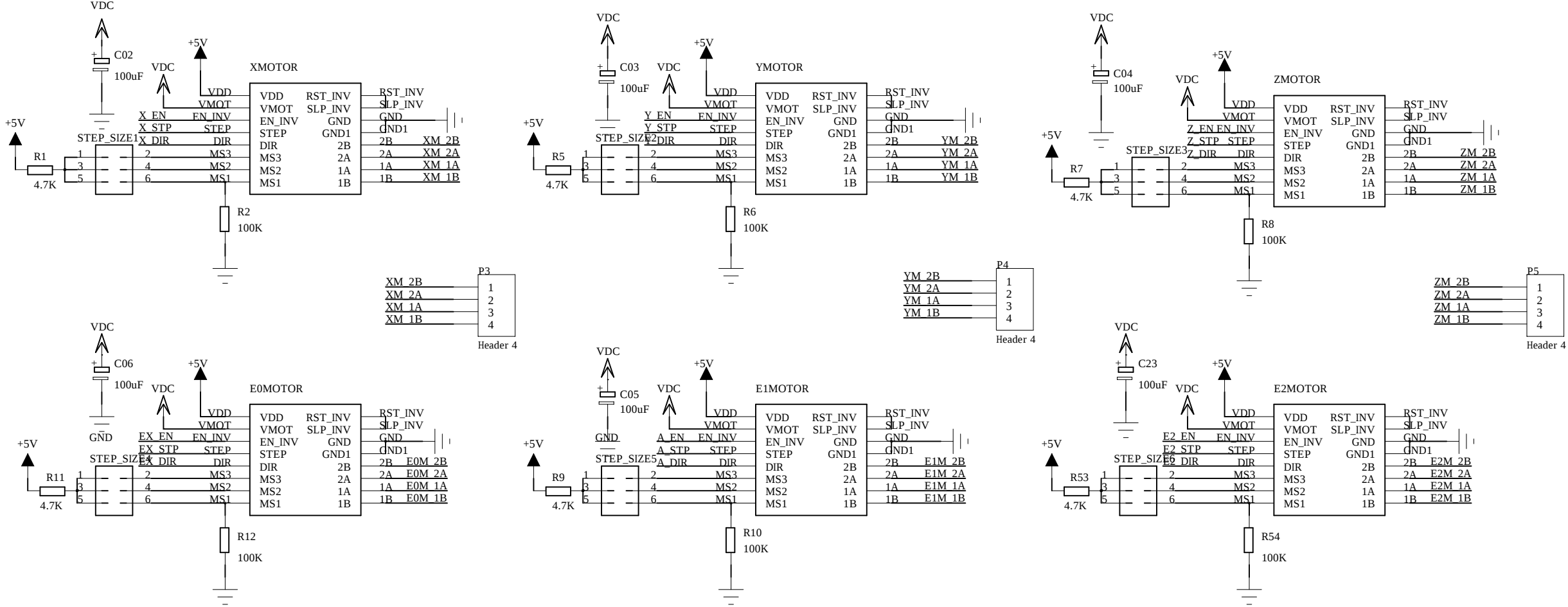
The schematic diagram illustrates the 3D TOUCH circuit. It features a 3-pin connector labeled J6. Pin 1 is connected to ground. Pin 2 is connected to a pull-up resistor and then to a +5V supply. Pin 3 is connected to a pull-up resistor and then to a +5V supply. The output of the pull-up resistor on pin 2 is labeled PB5. Below this, a Z_MIN signal is shown connected to pin 1 of a component labeled H6, which is also connected to ground.

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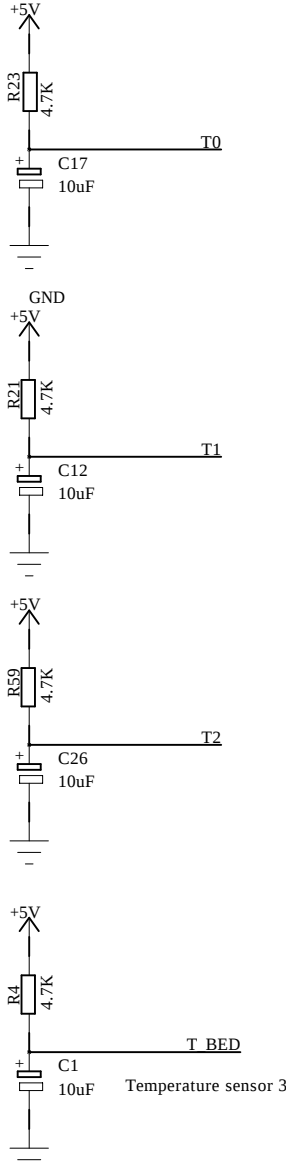
HOLE



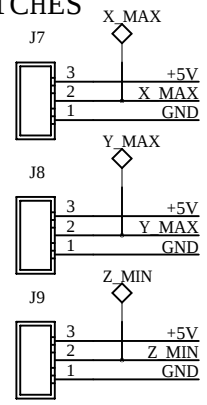
STEPPER DRIVER



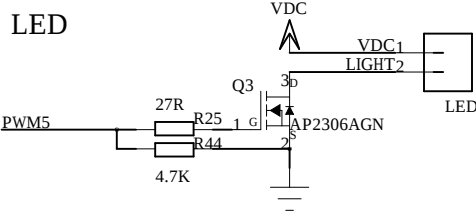
TEMPERATURE SENSOR



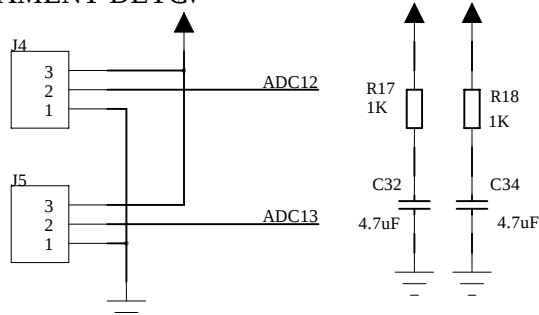
LIMIT SWITCHES



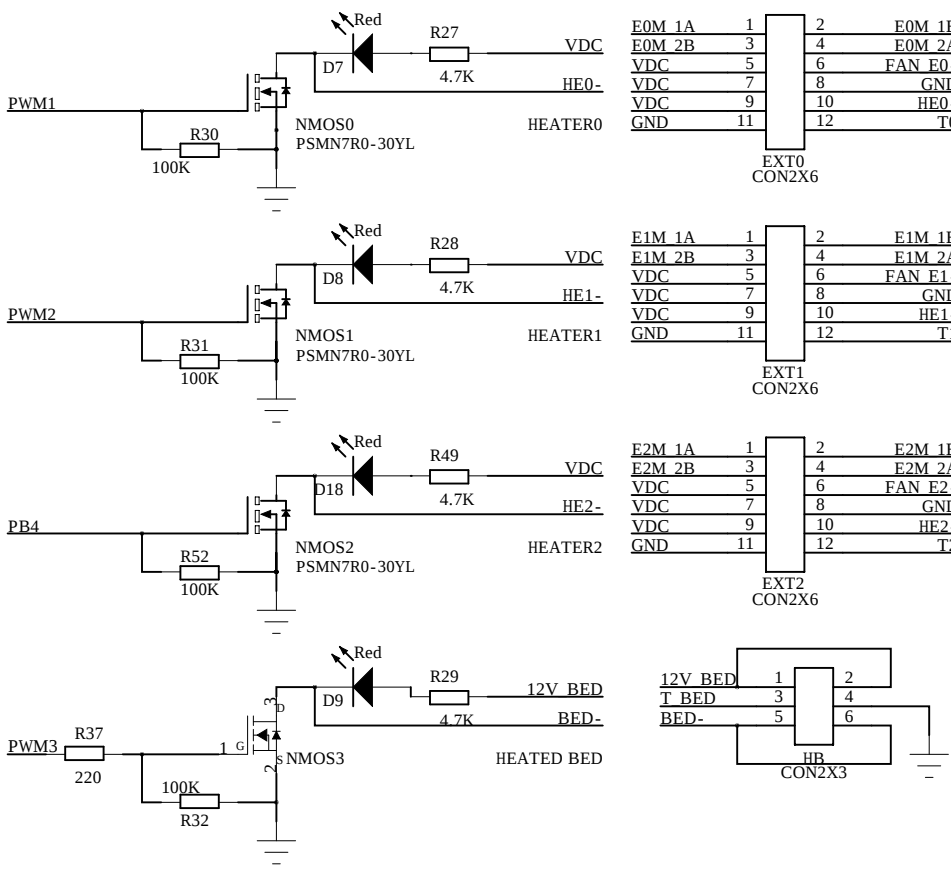
LED



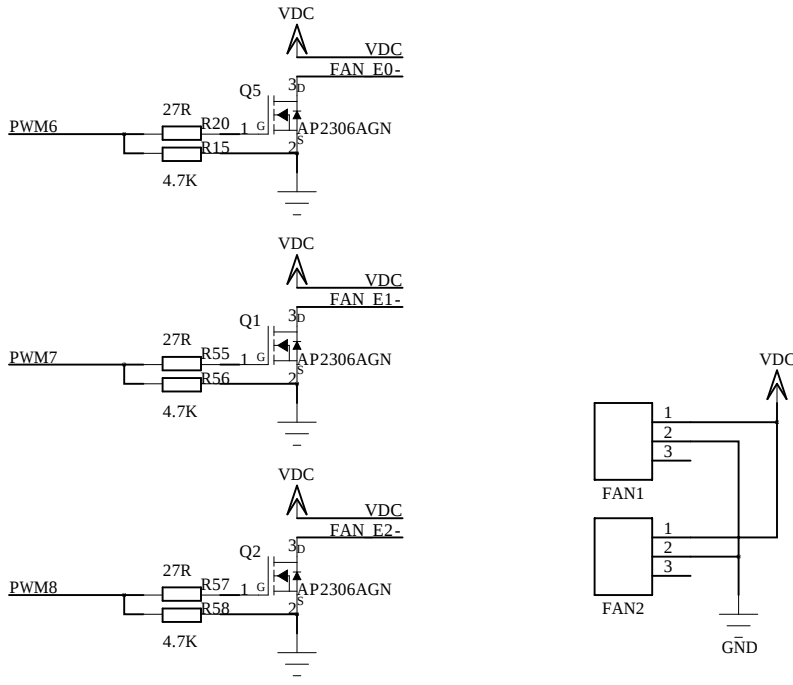
FILAMENT DETG



HOTBED & HOTEND CONTROL/CONNECTORS



FAN CONNECTORS



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